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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No.: 10/828,958

Filed:

April 20, 2004

For: A METHOD FOR MAKING A

SEMICONDUCTOR DEVICE HAVING A HIGH-K GATE DIELECTRIC LAYER AND A METAL GATE ELECTRODE Art Unit: unknown

Examiner: unknown

Attorney Docket: P18609

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **INFORMATION DISCLOSURE STATEMENT**

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with a copy of the reference cited on that form. It is respectfully requested that the cited reference be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: June 3 2004

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## FIRST CLASS CERTIFICATE OF MAILING

(37 C.F.R. § 1.8(a))

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